Amendments to the Claims:

This listing of claims will replace all prior versions, and listing, of claims in the application:

Listing of Claims:

1. (Currently amended) A liquid crystal display comprising:

a first insulating substrate;

a plurality of gate lines formed on the first insulating substrate;

a plurality of data lines insulated from the gate lines and intersecting the gate lines to define a plurality of pixel areas;

a plurality of pixel electrodes provided on the pixel areas;

a plurality of thin film transistors connected to the gate lines, the data lines and the pixel electrodes; a second insulating substrate facing the first insulating substrate;

a common electrode formed on the second insulating substrate;

a liquid crystal layer interposed between the first insulating substrate and the second insulating substrate and aligned in an OCB mode;

first and second compensation films provided on outer surfaces of the first and the second insulating substrate; and

first and second polarization films provided on outer surfaces of the first and the second compensation films,

wherein Rr $\leq$17 nm, Rg $\leq$15 nm and Rb $\leq$12 nm where Rr, Rg and Rb are retardations of the liquid crystal layer in a black state for red, green and blue lights, respectively.

2. - 5. (Canceled)

6. (Previously presented) A liquid crystal display comprising:

a first insulating substrate;

a plurality of gate lines formed on the first insulating substrate;

a plurality of data lines insulated from the gate lines and intersecting the gate lines to define a plurality of pixel areas;

a plurality of pixel electrodes provided on the pixel areas;

a plurality of thin film transistors connected to the gate lines, the data lines and the pixel electrodes; a second insulating substrate facing the first insulating substrate;

a common electrode formed on the second insulating substrate;

a liquid crystal layer interposed between the first insulating substrate and the second insulating substrate and aligned in an OCB mode;

first and second compensation films provided on outer surfaces of the first and the second insulating substrate; and

first and second polarization films provided on outer surfaces of the first and the second compensation films,

wherein a cell gap of the liquid crystal layer has different values on the red, the green and the blue pixel areas;

red, green and blue color filters disposed between the second insulating substrate and the common electrode, arranged corresponding to the red, the green and the blue pixel areas, respectively;

a gate insulating layer insulating the gate lines and the data lines; and

a passivation layer insulating the data lines and the pixel electrodes and protecting the thin film transistors,

wherein the green color filter is thicker than the red and the blue color filters and portions of the gate insulating layer and the passivation layer on the red and the green pixel areas are removed.

7. (Previously presented) A liquid crystal display comprising:

a first insulating substrate;

a plurality of gate lines formed on the first insulating substrate;

a plurality of data lines insulated from the gate lines and intersecting the gate lines to define a plurality of pixel areas;

a plurality of pixel electrodes provided on the pixel areas;

a plurality of thin film transistors connected to the gate lines, the data lines and the pixel electrodes; a second insulating substrate facing the first insulating substrate;

a common electrode formed on the second insulating substrate;

a liquid crystal layer interposed between the first insulating substrate and the second insulating substrate and aligned in an OCB mode;

first and second compensation films provided on outer surfaces of the first and the second insulating substrate; and

first and second polarization films provided on outer surfaces of the first and the second compensation films,

wherein a cell gap of the liquid crystal layer has different values on the red, the green and the blue pixel areas;

red, green and blue color filters disposed between the second insulating substrate and the common electrode, arranged corresponding to the red, the green and the blue pixel areas, respectively;

a gate insulating layer insulating the gate lines and the data lines; and

a passivation layer insulating the data lines and the pixel electrodes and protecting the thin film transistors,

wherein the green color filter is thicker than the red and the blue color filters and portions of the gate insulating layer and the passivation layer on the blue and the green pixel areas are removed.

8. (Canceled)